

### **Amendments to the Abstract**

Please replace the abstract of the disclosure with the following:

~~In accordance with the present invention there is those provided a~~ A Cartesian loop transmitter having ~~an~~ isolator eliminator circuitry ~~is presented, comprising~~ The isolator eliminator circuitry includes a set of low pass and wide band pass as well as narrow band pass filters for each of I- and Q-channels, root mean square detectors and dividers connected to a means for comparing. Signals from ~~said the~~ means for comparing are received by a microprocessor which controls attenuation setting. ~~There is also provided a method of adjusting~~ The output level of such the transmitter. ~~Said method comprises the step of~~ is adjusted by generating a small signal measuring an on-channel signal level and ~~said a~~ small signal level and then calculating a ~~first~~ ratio of ~~said the~~ small signal to ~~said the~~ on-channel signal. The attenuation setting is increased if the ~~If said first ratio exceeds a defined threshold said attenuation setting is increased.~~